

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Jun MURATA et al.
Application No.: Not yet assigned
Filed: February 10, 2004
For: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE HAVING
SWITCHING MISFET AND CAPACITOR ELEMENT AND
METHOD OF PRODUCING THE SAME, INLCUDING WIRING
THEREFOR AND METHOD OF PRODUCING SUCH WIRING
Group: Not yet assigned
Examiner: Not yet assigned

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

February 10, 2004

Sir:

Please amend the above-identified application, prior to examination thereof,
as listed in the following, and as set forth in the following pages:

Amendments to the Specification;

Remarks are included following the amendments.